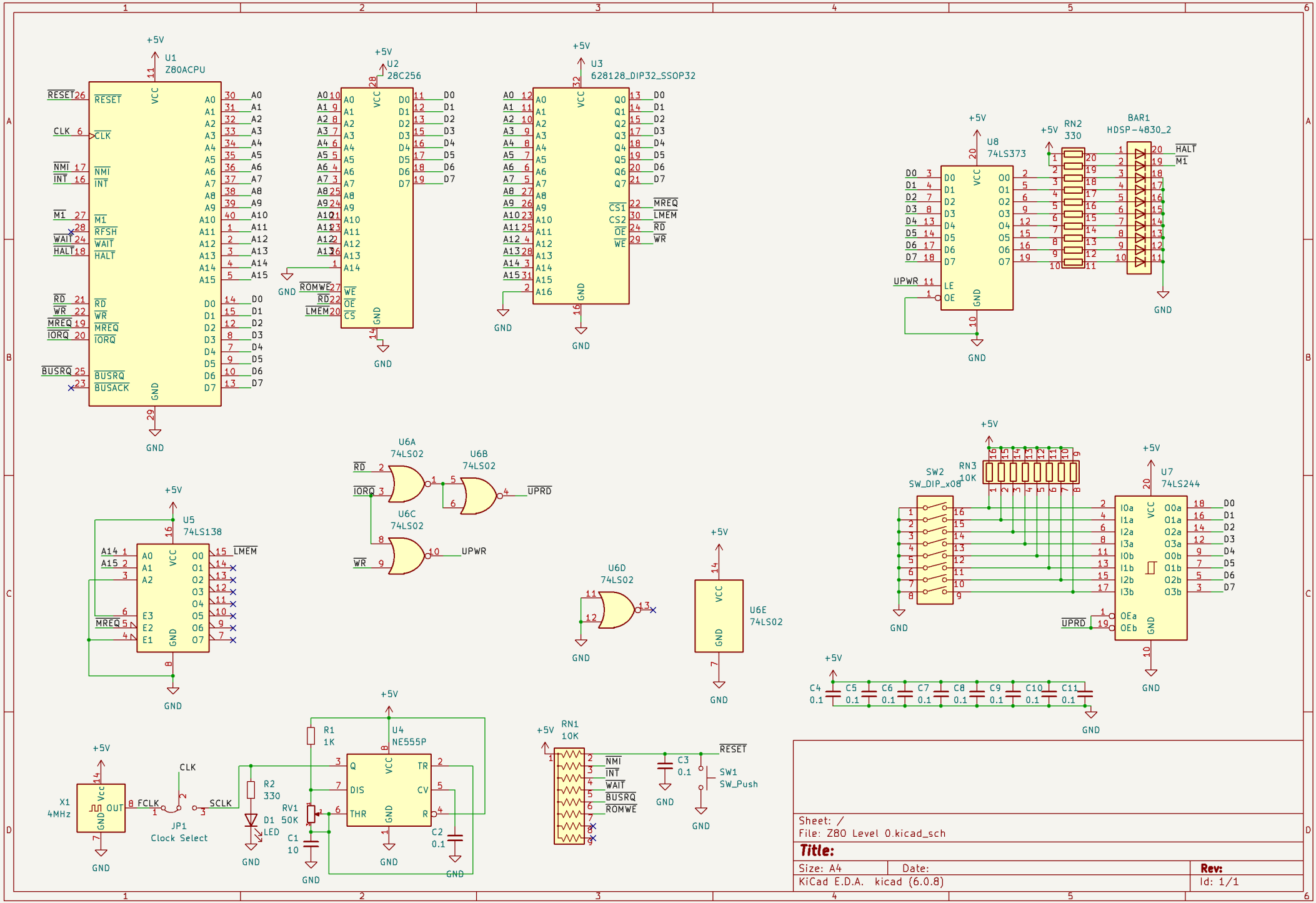
U1
Z80ACPU
U2
28C256
U3
628128_DIP32_SSOP32
U8
74LS373
RN2
330
BAR1
HDSP-4830_2
U5
74LS138
U6A
74LS02
U6B
74LS02
U6C
74LS02
U6D
74LS02
U6E
74LS02
SW2
SW_DIP_x08
RN3
10K
U7
74LS244
X1
4MHz
JP1
Clock Select
R1
1K
U4
NE555P
R2
330
D1
LED
RV1
50K
C1
10
C2
0.1
RN1
10K
C3
0.1
SW1
SW_Push
C4
0.1
C5
0.1
C6
0.1
C7
0.1
C8
0.1
C9
0.1
C10
0.1
C11
0.1
+5V
GND
RESET
CLK
NMI
INT
M1
WAIT
HALT
RD
WR
MREQ
IORQ
BUSRQ
BUSACK
ROMWE
LMEM
UPWR
UPRD
FCLK
SCLK
Sheet: /
File: Z80 Level 0.kicad_sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. kicad (6.0.8)
Id: 1/1